

Possibilities of implementation of elimination voltammetry in electrochemical analyzers

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Abstract

The study is focused on the implementation of elimination voltammetry with linear scan (EVLS) in electrochemical analyzers. This implementation consists in both hardware and software development. First, the hardware, involving the good potentiostat, must enable us to measure voltammetric curves at many different scan rates and with a suitable potential step. Second, the software should be able to provide a program not only for a voltammetric experiment, but also for the elimination procedure. The EVLS procedure needs the smoothing of voltammetric curves, the calculation of coefficients of chosen elimination functions and the presentation of elimination function in both numerical and graphical forms.

Keywords: voltammetry, electrochemical analyzers, potentiostat

Introduction

Thirteen years ago the theory of elimination voltammetry with linear scan (EVLS) was published and experimentally verified for selected electrode systems (Dracka, 1996; Trnkova and Dracka, 1996). To this date this method has found applications not only in electroanalysis, but also in the study of electrode processes of

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inorganic and organic electroactive substances at mercury, silver or graphite electrodes (Orinakova et al. 2004; Rozik and Trnkova 2006; Trnkova 2002; Trnkova et al. 2000). The EVLS can be considered as a mathematical model of the transformation of current–potential curves capable of eliminating some selected current components, while conserving others by means of elimination functions. For the calculation of the elimination functions three voltammetric curves at different scan rates should be recorded under identical experimental conditions. The attention was devoted to fundamentals of elimination voltammetry and to the transformation of irreversible current–potential curves of an adsorbed electroactive substance. The function, eliminating charging and kinetic current components and conserving the diffusion current component, yields specific, sensitive and well developed peak–counterpeak (p–cp) signal (Adam et al. 2005; Jelen et al. 2009; Mikelova et al. 2007a; Mikelova et al. 2007b; Trnkova 2002; Trnkova and Dracka 1996; Trnkova et al., 2006, 2003; Trnkova et al. 2000; Trnkova et al. 2004; Trnkova et al. 2008).

Materials and Methods

The home–made potentiostat was designed and constructed (Fig 1). New potentiostat has eight inputs for eight voltammetric experiments at the same time. The instrument can measure at five current ranges from 100 pA to 10 mA with smallest measurable value app. 5 pA. Internal battery increase noise immunity of the measurement and enable to provide measurement *in situ*. First voltammetric measurements were carried out with the VA–stand 663 (Metrohm, Switzerland). The standard cell consisted of three electrodes, a Hanging Mercury Drop Electrodes (HMDE) with an area of 0.4 mm² were employed as working electrode, an Ag/AgCl/3M KCl electrode served as the reference electrode and platinum wire electrode was used as the auxiliary electrode.

Results and Discussion

The potentiostat was primarily designed for detecting of various ions and molecules. Using the standard three electrode cell and cyclic voltammetry the first test was devoted to the redox processes of lead (Pb²⁺ in acetate buffer) and the second to the redox system of cadmium (Cd²⁺ in 0.1M KCl).



Figure 1: The eight-channel potentiostat. (See page number S22-S25)

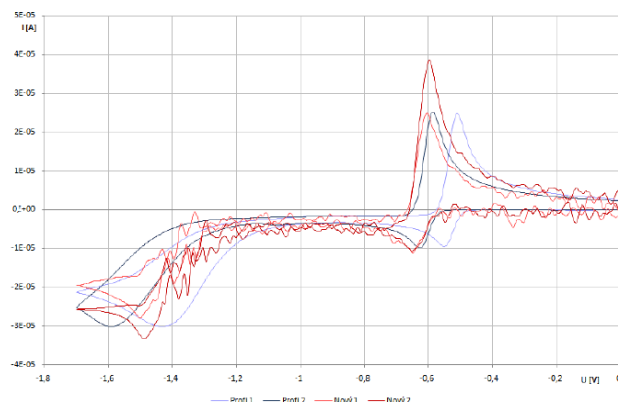


Figure 2: Cyclic voltammograms of redox couple Cd^{2+}/Cd in 0.1M KCl measured with (Autolab – blue lines) and home-made potentiostat (red lines). Scan rate 100 mV/s.

In addition to the measurements of chemical system tests with well-suited instrument inputs were carried out. The results (Fig 2) were discussed and the creation of the suitable filter circuit for suppressing noise in the measurement of real solutions was suggested. Moreover, the curves were successfully treated with elimination voltammetry implemented in the software package.

Conclusion

The small review about the possibilities of the implementation of elimination (EVLS) procedure in electrochemical analyzers was presented. The successful implementation of EVLS will need further the debugging software package and the depression of noise. The smoothing of voltammetric curves, the calculation of coefficients of chosen elimination functions and the graphic presentation of elimination function were designed.

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